



KAL - Large IP Cores:

Analog IP Cores:

- Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.
- We are expert in custom analog IP

CPU Cores:

- **8 bit - 8051**
- 8 bit- HC68HC11
- 8 bit - PIC Processor
- 8 bit – Z80
- 16 bit – D6800
- **DSP – MSP430**
- 32 bit - ARM 9xx/11xx

Memory Controllers:

- **SD/SDIO 2.0/3.0 Controller**
- SDRAM Controller
- DDR/DDR2/DDR3 SDRAM Controller
- NAND Flash Controller
- Flash/EEPROM/SRAM Controller
- PCMCIA/CompactFlash Host Adapter
- PCMCIA/CompactFlash Slave Controller

Clock Synchronization:

- IEEE 1588 Slave
- IEEE 1588 Master
- IEEE 1588 Master/Slave
- IEEE 1588 PTP Stack

New Oscillator Option for syn1588® NICs Development Board

Oregano Systems announces the availability of a new oscillator option for their syn1588® NIC family. Beside the standard oscillator and the high precision OCXO oscillator Oregano Systems now offers an TCXO oscillator for it's syn1588® NICs.

The quality of the local oscillator determines the accuracy of the IEEE1588 node in slave mode during network interruptions or under varying operating conditions (i.e. temperature). Turning on or off the fan in a PC or the air condition in the server rooms immediately affects the accuracy when looking for nanoseconds. On the other hands high quality oscillators are quite expensive.

Oregano Systems now offers three different oscillator options. This allows our customers to trade off costs versus accuracies for their applications.

Oscillator Options

- standard XO (50 ppm)
- **!! New !! TCXO (better than 4 ppm)**
- OCXO (better than 0,5 ppm)

The default configuration for all syn1588® NIC cards include the cheapest option "standard XO".

-> [Click here for Previews newsletter](#)

-> [Contact us](#) for more information:

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- IEEE 1588 L2/L3 Solution

Peripherals:

- Floating Point Unit
- I2C Master/Slave
- SPI Master/Slave
- CAN bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller
10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Slave
- On Chip Bus Analyzer

PCI Bus Controllers and Peripherals:

- PCI Express
- PCI-X Host Bridge
Master/Target
- PCI Host Bridge
Master/Target
- PCI-PCI Bridge
- PCI-ISA Bridge
- PCI Bus Arbiter

Encryption:

- AES 128bit/256bit
- ECC

AHB/APB Peripherals:

- AHB Bus Master/Slave
- APB Bus Master/Slave
- AHB/AXI DMA Controller

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Untill the next eNews,

Thanks yu for your attenstion.

KAL

- AXI Bus Master/Slave

MIPS CPU Interface:

- MIPS - SysAD Bus Slave
- MIPS - SysAD Bus to PCI Host bridge
- MIPS - EC interface to SDRAM Controller
- MIPS - EC Interface to PCI Host Bridge
- MIPS - EC Interface Bus Slave

PowerPC CPU Interface:

- Power PC Bus Master
- PowerPC to PCI Host bridge
- PowerPC Bus Arbiter
- PowerPC Bus Slave

ARC CPU Interface:

- ARC - Peripheral Controller for ARCTangent
- ARC – ARCTangent to PCI host Bridge

[Contact us for data sheet](#)

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