



KAL - Large IP Cores:

Analog IP Cores:

- **DDR2/3 PHY**
- Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.
- We are expert in custom analog IP

CPU Cores:

- **8 bit - 8051**
- 8 bit- HC68HC11
- 8 bit - PIC Processor
- 8 bit – Z80
- 16 bit – D6800
- **DSP – MSP430**
- 32 bit - ARM 9xx/11xx

Memory Controllers:

- **SD/SDIO 2.0/3.0 Controller**
- SDRAM Controller
- DDR/DDR2/DDR3 SDRAM Controller
- NAND Flash Controller
- Flash/EEPROM/SRAM Controller
- PCMCIA/CompactFlash Host Adapter
- PCMCIA/CompactFlash Slave Controller

Clock Synchronization:

- IEEE 1588 Slave
- IEEE 1588 Master
- IEEE 1588 Master/Slave

syn1588® NIC Development Boards: Twice the Accuracy

Oregano Systems (<http://www.oreganosystems.at>) released a new FPGA firmware version for its syn1588® NICs that achieves twice the accuracy of its timestamps. The increased performance is available both for timestamps of external events as well as for Ethernet network timestamps. This enhancement is available for both the PCI as well as the PCIe version of the syn1588® NICs.

During the last months the new structures had been tested intensively in our labs in order to ensure stable quality of our products. The new FPGA firmware version does not require a software update although we recommend our customers to switch to the latest version of the syn1588® PTP Stack.

This demonstrates Oregano Systems dedication to deliver maximum clock synchronisation accuracies for their customers.

Contact us for more information.

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- IEEE 1588 PTP Stack
- IEEE 1588 L2/L3 Solution

Peripherals:

- Floating Point Unit
- I2C Master/Slave
- SPI Master/Slave
- CAN bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller
10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Slave
- On Chip Bus Analyzer

PCI Bus Controllers and
Peripherals:

- PCI Express
- PCI-X Host Bridge
Master/Target
- PCI Host Bridge
Master/Target
- PCI-PCI Bridge
- PCI-ISA Bridge
- PCI Bus Arbiter

Encryption:

- AES 128bit/256bit
- ECC

AHB/APB Peripherals:

- AHB Bus Master/Slave
- APB Bus Master/Slave

Untill the next eNews,

Thanks yu for your attention.

KAL

- AHB/AXI DMA Controller
- AXI Bus Master/Slave

MIPS CPU Interface:

- MIPS - SysAD Bus Slave
- MIPS - SysAD Bus to PCI Host bridge
- MIPS - EC interface to SDRAM Controller
- MIPS - EC Interface to PCI Host Bridge
- MIPS - EC Interface Bus Slave

PowerPC CPU Interface:

- Power PC Bus Master
- PowerPC to PCI Host bridge
- PowerPC Bus Arbiter
- PowerPC Bus Slave

ARC CPU Interface:

- ARC - Peripheral Controller for ARCTangent
- ARC – ARCTangent to PCI host Bridge

[Contact us for data sheet](#)