



KAL - Large IP Cores:

Analog IP Cores:

- **DDR2/3 PHY**
- Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.
- We are expert in custom analog IP

CPU Cores:

- **8 bit - 8051**
- 8 bit- HC68HC11
- 8 bit - PIC Processor
- 8 bit – Z80
- 16 bit – D6800
- **DSP – MSP430**
- 32 bit - ARM 9xx/11xx

Memory Controllers:

- **SD/SDIO 2.0/3.0 Controller**
- SDRAM Controller
- DDR/DDR2/DDR3 SDRAM Controller
- NAND Flash Controller
- Flash/EEPROM/SRAM Controller
- PCMCIA/CompactFlash Host Adapter
- PCMCIA/CompactFlash Slave Controller

Clock Synchronization:

- IEEE 1588 Slave
- IEEE 1588 Master
- IEEE 1588 Master/Salve

2010 marks 17th consecutive year of profitability for developer of visualization and debugging technology

Concept Engineering, developer of visualization and debugging technology for the electronic design automation (EDA) market, celebrates the 20th year of providing customers with leading-edge visualization and debugging technology. The company attributes its success and its 17th consecutive year of profitability to innovative technology and long-term commitment to creating value for customers.

Today, more than 35 original equipment manufacturers (OEMs) use Concept's schematic generation and visualization technology – Nlview™ – as part of their products or design flows. Of the top 20 semiconductor companies, 18 use debugging tools from Concept – SpiceVision® PRO or GateVision® PRO or RTLvision® PRO. Concept Engineering's transistor-level debugging tool, SpiceVision PRO, is in use at more than 100 semiconductor and electronics companies world-wide.

Founders Gerhard Angst, president and CEO, and Lothar Linhard, CTO and vice president of engineering, have been managing the company since inception in 1990. "With 17 years of profitability and zero debt, the company has been able to maintain a very independent and innovative start-up-like environment," said Gerhard Angst. "We've always been committed to keeping customers happy for the long term, and are fortunate in having a group of passionate and highly motivated employees with the enthusiasm to create leading-edge software. The most rewarding aspect of the past 20 years has been the confidence our growing customer base has shown in the company and our software."

"A worldwide base of customers that continue to invest in successive Concept Engineering speaks to our continuing commitment to technology leadership," said Lothar Linhard. "We are committed to innovation, now providing visualization technology for all design levels, including system level, RTL level, gate level and transistor level."

In 2007, Concept Engineering was singled out by EDACentrum, an independent association dedicated to the promotion of research and development in EDA, to receive the EDA Achievement Award for achievements in research and development. Each year the EDA Achievement Award is given by EDACentrum to a team or individual who

- IEEE 1588 PTP Stack
- IEEE 1588 L2/L3 Solution

Peripherals:

- Floating Point Unit
- I2C Master/Slave
- SPI Master/Slave
- CAN bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller 10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Slave
- On Chip Bus Analyzer

PCI Bus Controllers and Peripherals:

- PCI Express
- PCI-X Host Bridge Master/Target
- PCI Host Bridge Master/Target
- PCI-PCI Bridge
- PCI-ISA Bridge
- PCI Bus Arbiter

Encryption:

- AES 128bit/256bit
- ECC

AHB/APB Peripherals:

- AHB Bus Master/Slave
- APB Bus Master/Slave

reached excellent results in research or development within the EDA field and who contributed to the improvements within the Ekompass Project. (Ekompass is a funding program set up by the German Ministry of Education and Research (BMBF) to stimulate EDA research. Ekompass means "design platform for complex applied systems and circuits in microelectronics" in German.).

About Concept Engineering:

Concept Engineering is a privately held company based in Freiburg, Germany, founded in 1990 to develop and market innovative schematic generation and viewing technology for use with logic synthesis, verification, test automation and physical design tools. The company's customers are primarily original equipment EDA tool manufacturers (OEMs), in-house CAD tool developers and semiconductor companies. For more information please see: <http://www.concept.de>.

Contact us for more information.

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 Untill the next eNews,

Thanks yu for your attenstion.

KAL

- AHB/AXI DMA Controller
- AXI Bus Master/Slave

MIPS CPU Interface:

- MIPS - SysAD Bus Slave
- MIPS - SysAD Bus to PCI Host bridge
- MIPS - EC interface to SDRAM Controller
- MIPS - EC Interface to PCI Host Bridge
- MIPS - EC Interface Bus Slave

PowerPC CPU Interface:

- Power PC Bus Master
- PowerPC to PCI Host bridge
- PowerPC Bus Arbiter
- PowerPC Bus Slave

ARC CPU Interface:

- ARC - Peripheral Controller for ARCTangent
- ARC – ARCTangent to PCI host Bridge

[Contact us for data sheet](#)

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