



I wanted to make sure that you will get all updated information from our vendors participant at the [DAC](#) in San Francisco next week. If you visit the [DAC](#) conference make sure you visit our vendors's booths. I will be there, I can setup a meeting for you. Let me know.

Adi Katav

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## **Memory Library IP Cores from SDS:**

[SDS](#) (Silicon Design Solutions Inc) is an IP and Design Services Provider. Headquartered in Milpitas California USA Offering a wide range of Embedded Memories delivered as a Compiler or as a single Instance. SRAM, ROM, MPRF, CAM & TLB products available on Leading Foundry processes from 250nm to 28nm. Design Services include Custom Embedded Memory Compilers or Instances Optimized to meet Customer application, Complete Test Chip development, Bit Cell development and contracted augmentation of internal memory design teams. Engagement/Business Models are flexible and varied.

[Visit Booth 1410.](#)

## **Visualisation and Debugging ASIC EDA tools by Concept Engineering:**

[Concept Engineering](#) provides visualization technology for the EDA market. Our automatic schematic generation, navigation and viewing technology is available as software component [Nlview](#) for EDA tool providers as well as software tools [RTLvision](#), [GateVision](#), [SpiceVision](#) and [SGvision](#) for electronic design engineers.

Concept software helps engineers to understand, debug, document and interact with electronic design data. It fits into many spots in existing and future design flows, wherever displaying schematic diagrams (usually excerpts like cones,

paths, etc) is helpful for understanding a certain situation or problem quickly. Our technology is used in many fields, including synthesis, verification, post-layout analysis, parasitic network exploration, LVS, etc.

[Visit booth 918.](#)

## **About KAL:**

We provide our customer with professional consultancy and ASIC [Program Management](#), [Consultancy Service](#). KAL has worldwide partners to provide [Digital /RF/Analog ASIC Design Service](#), [Supply Chain Service](#), [Silicon IP Cores](#), [EDA Tools](#) as well as [On site Engineering Service](#).

## **KAL's Services:**

- [FPGA/Digital ASIC Design](#)
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Until then, we thank you for your continued support!

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