Debugging Tools for IC, SoC and FPGA Designers

Concept Engineering's debugging and visualization software helps electronic design engineers to easily understand, debug, optimize and document electronic designs. StarVision, RTLvision, GateVision, SpiceVision and SgVision are customizable debugging tools for electronic design engineers. Our automatic schematic generation, navigation and viewing technology Nlview/T-engine is available as software component/library for EDA tool developers. Our technology is used in many fields, including: RTL development, IP reuse, ASIC and SoC design, Analog mixed-signal design, synthesis, verification, post-layout analysis, System/RTL/netlist debugging and visualization.

Learn more and see a self demonstration

About Concept Engineering:

Concept Engineering is a privately held company based in Freiburg, Germany, founded in 1990 to develop and market innovative schematic generation and viewing technology for use with logic synthesis, verification, circuit characterization, circuit optimization, test automation and physical design tools. The company's customers are primarily EDA tool manufacturers (OEMs), in-house CAD tool developers and semiconductor companies.
IEEE 1588 PTP Stack
IEEE 1588 L2/L3 Solution

Peripherals:
- Floating Point Unit
- I2C Master/Slave
- SPI Master/Slave
- CAN bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller
- 10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Slave
- On Chip Bus Analyzer

PCI Bus Controllers and Peripherals:
- PCI Express
- PCI-X Host Bridge
- Master/Target
- PCI Host Bridge
- Master/Target
- PCI-PCI Bridge
- PCI-ISA Bridge
- PCI Bus Arbiter

Encryption:
- AES 128bit/256bit
- ECC

AHB/APB Peripherals:
- AHB Bus Master/Slave
- APB Bus Master/Slave

Contact us for more information:
by phone: 04-6201129 Ext 4
by fax: 04-6201328
by email: info@kaltech.co.il
by Web: www.kaltech.co.il
by skype: adi_katav
by Facebook: kal silicon

Contact via LinkedIn http://www.linkedin.com/pub/adi-katav/30/b57/b1a

---------------------------------------------------------------------------------------------------

Untill the next eNews,

Thanks yu for your attenstion.

KAL
• AHB/AXI DMA Controller
• AXI Bus Master/Slave

**MIPS CPU Interface:**
• MIPS - SysAD Bus Slave
• MIPS - SysAD Bus to PCI Host bridge
• MIPS - EC interface to SDRAM Controller
• MIPS - EC Interface to PCI Host Bridge
• MIPS - EC Interface Bus Slave

**PowerPC CPU Interface:**
• PowerPC Bus Master
• PowerPC to PCI Host bridge
• PowerPC Bus Arbiter
• PowerPC Bus Slave

**ARC CPU Interface:**
• ARC - Peripheral Controller for ARChangent
• ARC – ARChangent to PCI host Bridge

Contact us for data sheet

**Contact details:**
Tel +972-4-6201129 Ext: 4
Fax +972-4-6201328

[www.KALtech.co.il](http://www.KALtech.co.il)

info@kaltech.co.il

eNews registration: [http://www.kaltech.co.il/](http://www.kaltech.co.il/)

KAL Katav Associates Silicon Technologies - POB 2206 Hadera 38121 Israel (C) 2012