



KAL - Large IP Cores:

Analog IP Cores:

- Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.
- We are expert in custom analog IP

CPU Cores:

- 8 bit - 8051
- 8 bit - 80251
- 8 bit- HC68HC11
- 8 bit - PIC Processor
- 8 bit – Z80
- 16 bit – D6800
- DSP – MSP430
- 32 bit - ARM 9xx/11xx

Memory Controllers:

- SD/SDIO 2.0/3.0 Controller
- SDRAM Controller
- DDR/DDR2/DDR3 SDRAM Controller
- NAND Flash Controller
- Flash/EEPROM/SRAM Controller
- PCMCIA/CompactFlash Host Adapter
- PCMCIA/CompactFlash Slave Controller

Clock Synchronization:

- IEEE 1588 Slave
- IEEE 1588 Master
- IEEE 1588 Master/Slave

Debugging Tools for IC, SoC and FPGA Designers

Concept Engineering's debugging and visualization software helps electronic design engineers to easily understand, debug, optimize and document electronic designs. [StarVision](#), [RTLvision](#), [GateVision](#), [SpiceVision](#) and [SGvision](#) are customizable debugging tools for electronic design engineers. Our automatic schematic generation, navigation and viewing technology [Nlview/T-engine](#) is available as software component/library for EDA tool developers. Our technology is used in many fields, including: RTL development, IP reuse, ASIC and SoC design, Analog mixed-signal design, synthesis, verification, post-layout analysis, System/RTL/netlist debugging and visualization.

[Learn more and see a self demonstration](#)

About Concept Engineering:

Concept Engineering is a privately held company based in Freiburg, Germany, founded in 1990 to develop and market innovative schematic generation and viewing technology for use with logic synthesis, verification, circuit characterization, circuit optimization, test automation and physical design tools. The company's customers are primarily EDA tool manufacturers (OEMs), in-house CAD tool developers and semiconductor companies.

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- IEEE 1588 PTP Stack
- IEEE 1588 L2/L3 Solution
- Peripherals:
- Floating Point Unit
- I2C Master/Slave
- SPI Master/Slave
- CAN bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller 10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Slave
- On Chip Bus Analyzer
- PCI Bus Controllers and Peripherals:
- PCI Express
- PCI-X Host Bridge Master/Target
- PCI Host Bridge Master/Target
- PCI-PCI Bridge
- PCI-ISA Bridge
- PCI Bus Arbiter
- Encryption:
- AES 128bit/256bit
- ECC
- AHB/APB Peripherals:
- AHB Bus Master/Slave
- APB Bus Master/Slave

-> [Contact us](#) for more information:

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Untill the next eNews,

Thanks yu for your attenstion.

KAL

- AHB/AXI DMA Controller
- AXI Bus Master/Slave

MIPS CPU Interface:

- MIPS - SysAD Bus Slave
- MIPS - SysAD Bus to PCI Host bridge
- MIPS - EC interface to SDRAM Controller
- MIPS - EC Interface to PCI Host Bridge
- MIPS - EC Interface Bus Slave

PowerPC CPU Interface:

- Power PC Bus Master
- PowerPC to PCI Host bridge
- PowerPC Bus Arbiter
- PowerPC Bus Slave

ARC CPU Interface:

- ARC - Peripheral Controller for ARCTangent
- ARC – ARCTangent to PCI host Bridge

[Contact us for data sheet](#)

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