



**KAL - Large IP Cores:**

Analog IP Cores:

- Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.
- We are expert in custom analog IP

CPU Cores:

- 8 bit - 8051
- 8 bit- HC68HC11
- 8 bit - PIC Processor
- 8 bit – Z80
- 16 bit – D6800
- DSP – MSP430
- 32 bit - ARM 9xx/11xx

Memory Controllers:

- SD/SDIO 2.0/3.0 Controller
- SDRAM Controller
- DDR/DDR2/DDR3 SDRAM Controller
- NAND Flash Controller
- Flash/EEPROM/SRAM Controller
- PCMCIA/CompactFlash Host Adapter
- PCMCIA/CompactFlash Slave Controller

Clock Synchronization:

- IEEE 1588 Slave
- IEEE 1588 Master
- IEEE 1588 Master/Slave
- IEEE 1588 PTP Stack

**Digital Core Design, as a leading IP Core provider and System-on-Chip (SoC) design house, has been invited for a conference "Know-how transfer in automotive". The conference was held during international Fair of Automation and Robotization in Industry at Expo Silesia, Poland.**

Digital Core Design as a proprietary automotive IP Core provider participated in fair show and the international conference devoted to know-how transfer. Poland and Silesia Region particularly is one of the leading country regarding to automotive business in Europe. Thanks to the main European FIAT fab and OPEL (General Motors) factory which manufacture their newest car models, growth rate is positive, even though economy crisis. Strict cooperation with concerns propels also proprietary solutions providers. – *We noticed that safety & power safe technology are becoming one of the most significant, not only in automotive* – says Tomasz Krzyzak, vice-president at Digital Core Design – *that's why not only our CPU solutions but also "automotive" interfaces CAN or LIN are equipped with enhanced control and power safe solutions.*

Like for example CAN is a keyword connecting the world of electronics with the world of automotive. Because of its fundamental role in all aspects of security and safety, trustworthy implementations are crucial. That's why Digital Core Design developed unique IP Core, which delimits the highest quality standards. The DCAN is a standalone controller for the Controller Area Network (CAN), which is common used in automotive and industrial applications. What's the most important, DCAN conforms to Bosch CAN 2.0B specification (2.0B Active). The Core has simple CPU interface (8/16/32 bit configurable data width), with little or big endian addressing scheme. The DCAN supports both standard (11 bit identifier) and extended (29 bit identifier) frames. Hardware message filtering and 64 byte receive FIFO enables back-to-back message reception with minimum CPU load. The DCAN is described at RTL level allowing target use in FPGA or ASIC technologies.

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- IEEE 1588 L2/L3 Solution

Peripherals:

- Floating Point Unit
- I2C Master/Slave
- SPI Master/Slave
- CAN bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller 10/100/1000 BaseT

- DMA Controller
- USB 1.0/2.0 Host/Slave
- On Chip Bus Analyzer

PCI Bus Controllers and Peripherals:

- PCI Express
- PCI-X Host Bridge Master/Target
- PCI Host Bridge Master/Target
- PCI-PCI Bridge
- PCI-ISA Bridge
- PCI Bus Arbiter

Encryption:

- AES 128bit/256bit
- ECC

AHB/APB Peripherals:

- AHB Bus Master/Slave
- APB Bus Master/Slave
- AHB/AXI DMA Controller

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Untill the next eNews,

Thanks yu for your attenstion.

KAL

- AXI Bus Master/Slave

MIPS CPU Interface:

- MIPS - SysAD Bus Slave
- MIPS - SysAD Bus to PCI Host bridge
- MIPS - EC interface to SDRAM Controller
- MIPS - EC Interface to PCI Host Bridge
- MIPS - EC Interface Bus Slave

PowerPC CPU Interface:

- Power PC Bus Master
- PowerPC to PCI Host bridge
- PowerPC Bus Arbiter
- PowerPC Bus Slave

ARC CPU Interface:

- ARC - Peripheral Controller for ARCTangent
- ARC – ARCTangent to PCI host Bridge

[Contact us for data sheet](#)

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