



KAL - Large IP Cores:

Memory Controllers:

- **SD/SDIO 2.0/3.0 Controller**
- SDRAM Controller
- **DDR/DDR2/DDR3 SDRAM Controller**
- NAND Flash Controller
- Flash/EEPROM/SRAM Controller
- PCMCIA/CompactFlash Host Adapter
- PCMCIA/CompactFlash Slave Controller

CPU Cores:

- **32 bit - NEW**
- **8 bit - 8051**
- 8 bit- HC68HC11
- 8 bit - PIC Processor
- 8 bit – Z80
- 16 bit – D6800

Clock Synchronization:

- IEEE 1588 Slave
- IEEE 1588 Master
- IEEE 1588 Master/Slave
- IEEE 1588 PTP Stack
- IEEE 1588 L2/L3 Solution

Peripherals:

- HDLC/SDLC
- Smart Card Reader Unit
- EEPROM SPI Ctrl
- LCD Ctrl
- Floating Point Unit
- I2C Master/Slave
- SPI Master/Slave

Dear <Salutation>,

Concept Engineering Adds JavaScript-based Web Capabilities to Nview at DAC 2016

NviewJS enables automatic schematic generation, schematic viewing and automated system assembly to run on web browsers

Concept Engineering, specialists in visualization and debugging technology for electronic circuits and systems, will introduce the newest addition to the Nview™ automatic schematic generation family of products at DAC 2016. NviewJS™ is a schematic engine that brings automatic schematic generation, schematic viewing and automated system assembly to the web. The new visualization engine is based on JavaScript and HTML5 standards and enables

- CAN bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller 10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Slave
- On Chip Bus Analyzer

PCI Bus Controllers and Peripherals:

- PCI Express
- PCI-X Host Bridge Master/Target
- PCI Host Bridge Master/Target
- PCI-PCI Bridge
- PCI-ISA Bridge
- PCI Bus Arbiter

Modulation:

- ADPSM

AHB/APB Peripherals:

- AHB Bus Master/Slave
- APB Bus Master/Slave
- AHB/AXI DMA Controller
- AXI Bus Master/Slave

Analog IP Cores:

- Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.
- We are expert in custom analog IP

[Contact us for data sheet](#)

interactive tools for design debugging, design visualization and automated design creation that seamlessly run within standard web browsers on mobile devices or on desktop systems.

Concept Engineering's Nlview engine provides automatic generation of schematic diagrams for different levels of electronic circuits, including gate level, RTL level and block level. Optional engines are available for the system level (S-engine) and for the transistor level (T-engine). The new NlviewJS brings design engineers or service engineers the convenience of using web browsers to remotely access design data, intellectual property (IP) library components or system diagrams without having to install complex software on their computer or mobile device. Web-based design and debugging tools allow multiple users to have accurate access to

up-to-date, live system data and offer interactive communication with connected data servers.

NlviewJS gives designers the ability to use company- and application-specific Intranet tools, without any need to install complex software. Simply pointing the browser to the correct URL will provide access to design and critical, up-to-date system “health” information from any location. Visualization features and debugging capabilities are customizable, allowing company-specific design and service platforms.

“Many of our customers in the electronic design automation, automotive and industrial markets have been asking for web-based capabilities,” said Gerhard Angst, CEO and president of Concept Engineering. “Flexible and quick access to centralized design data and visualization of live system

status are challenges within larger organizations or for service engineers that work remotely. NlviewJS will help them overcome those challenges.” NlviewJS is available now. It and all the company’s schematic generation, visualization and debugging tools for

EDA tool developers will be demonstrated in the Concept Engineering booth (#1229) at DAC 2016 in the

Austin Convention Center, Austin, Texas from June 6th to 8th.

About Concept Engineering

Concept Engineering is a privately-held company based in Freiburg, Germany, that provides visualization and debugging technology for electronic circuits and systems, including automatic schematic generation technology for all major design levels. The company’s technology helps electronic design engineers to easily

understand, debug, optimize and document electronic designs. Concept Engineering's software technology is used in many fields in the EDA market, including: RTL development, IP reuse, ASIC and SoC design, FPGA design, analog/ mixed-signal design, logic synthesis, design verification, test automation, post-layout analysis, debugging and visualization at system level, RTL level, netlist level and transistor level.

KAL is representing Concept Engineering GmbH.

www.KALtech.co.il

We are looking forward to hear from you.
Contact us for more information.

Tel +972-4-6201129

<http://www.KALtech.co.il>

info@kaltech.co.il

[Follow on LinkedIn](#)

Facebook: kal silicon

eNews Registration: <http://www.kaltech.co.il/>

Until the next eNews,

Thanks you for your attention.



KAL

SpiceVision PRO, GateVision PRO, RTLvision PRO, and StarVision PRO are registered trademarks and Nlview, NlviewJS, T-engine and S-engine are trademarks of Concept Engineering GmbH. Concept Engineering and Verific Design Automation acknowledge trademarks or registered trademarks of other organizations for their respective products and services.