



KAL - Large IP Cores:

Analog IP Cores:

- Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.
- We are expert in custom analog IP

CPU Cores:

- **8 bit - 8051**
- 8 bit- HC68HC11
- 8 bit - PIC Processor
- 8 bit – Z80
- 16 bit – D6800

- **DSP – MSP430**

Memory Controllers:

- **SD/SDIO 2.0/3.0 Controller**
- SDRAM Controller
- DDR/DDR2/DDR3 SDRAM Controller
- NAND Flash Controller
- Flash/EEPROM/SRAM Controller
- PCMCIA/CompactFlash Host Adapter
- PCMCIA/CompactFlash Slave Controller

Clock Synchronization:

- IEEE 1588 Slave
- IEEE 1588 Master
- IEEE 1588 Master/Slave
- IEEE 1588 PTP Stack
- IEEE 1588 L2/L3 Solution

HDLC/SDLC IP Core

The DHDLC is the HDLC/SDLC controller used for supporting CPU in range of controlling HDLC/SDLC transmission frame. The IP core has been designed to be used with great variety of microcontrollers, no matter if it's 8-, 16- or 32-bit. It allows to control the frame on level of bits and flags, control frame errors, recognize and insert address byte(s), buffer received or transmitted data in separated FIFOs.

The DHDLC has a capability to **configure** many HDLC/SDLC **frame features** like number of address bytes, CRC, flags insertion, idle detection, pad filling, NRZI coding, etc. It allows to use the **IP Core in many different applications**. DCD's trustworthy Core has also configurable interrupt output which is driven by frame and control events. What does it mean for you? Time is money, so thanks to the DHDLC IP Core you can save MCU time wasted for handling HDLC/SDLC frames.

We are looking forward to hear from you.

Contact us for more information.

Peripherals:

- Smart Card Reader Unit
- Floating Point Unit
- I2C Master/Slave
- SPI Master/Slave
- CAN bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller
10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Slave
- On Chip Bus Analyzer

PCI Bus Controllers and Peripherals:

- PCI Express
- PCI-X Host Bridge
Master/Target
- PCI Host Bridge
Master/Target
- PCI-PCI Bridge
- PCI-ISA Bridge
- PCI Bus Arbiter

Encryption:

- AES 128bit/256bit
- ECC

AHB/APB Peripherals:

- AHB Bus Master/Slave
- APB Bus Master/Slave
- AHB/AXI DMA Controller

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Until the next eNews,

Thanks you for your attention.

KAL

- AXI Bus Master/Slave

MIPS CPU Interface:

- MIPS - SysAD Bus Slave
- MIPS - SysAD Bus to PCI Host bridge
- MIPS - EC interface to SDRAM Controller
- MIPS - EC Interface to PCI Host Bridge
- MIPS - EC Interface Bus Slave

PowerPC CPU Interface:

- Power PC Bus Master
- PowerPC to PCI Host bridge
- PowerPC Bus Arbiter
- PowerPC Bus Slave

ARC CPU Interface:

- ARC - Peripheral Controller for ARCTangent
- ARC – ARCTangent to PCI host Bridge

[Contact us for data sheet](#)

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