

# IEEE1588® Day 2010

## Training for SW/HW engineers seeking knowledge on the new IEEE1588® standard for clock syncing over Ethernet.

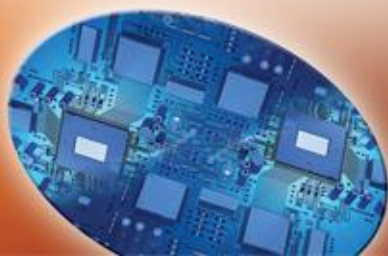
**Date:** March 17th 2010

**Location:** [Israeli Air-Force Center](#), Hertzelia, Israel

**Lecturer:** Mr. Nikolaus Kerö, General Manager of [Oregano Systems GmbH](#), Austria, specialized in IEEE1588® standard.

### **Day Agenda:**

- 09:00** Reception & Registration
- 09:15** General Introduction
- 09:30** Introduction into network based clock sync according to IEEE1588®  
System requirements & application examples  
Basic principles of clock sync  
NTP in a nutshell  
Comparison HW-SW solutions  
Details on the IEEE1588® standard (version 1.0 vs. version 2.0)  
Network related topics of clock synchronization  
Application requirements: Everything is synchronized, what do we do now?
- 11:30** Traps & Pitfall of highly accurate clock synchronization  
System related issues to cope with if sub 10ns accuracies are targeted  
Hardware and IP core implementation related issues  
FPGA vs ASIC implementations
- 12:30** Lunch
- 13:00** Software for clock synchronization  
PTP Stack  
Drivers  
Layer 2 implementation
- 14:30** Oregano Systems SYN1588® product portfolio
- 14:45** Q&A and one on one sessions.



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**Registration:**

Registration form can be downloaded on line from:

[http://www.kaltech.co.il/pdf/1588\\_Registration\\_form.doc](http://www.kaltech.co.il/pdf/1588_Registration_form.doc)

Please fill it in and fax it to: 04-6201328, or email it to: [kal@kaltech.co.il](mailto:kal@kaltech.co.il)

**Price**

1,200 NIS+ V.A.T include: CDROM with all presentations, application notes and articles, Printed hand outs, Beverages during the entire day and lunch.

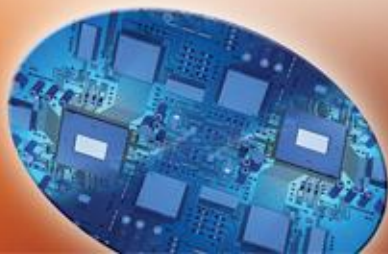
Payment prior the IEEE1588® Day by Check or wire transferring.

I'm looking forward to see you!

Adi Katav  
General Manager

Note- the organizers have the right to change or cancel the training with a written notice.

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