

KAL - Large IP Cores:

VIP cores:

- PCIe, USB, AMBA
- SD/SDIO, eMMC
- UniPro, I3C, Soundwire
- DDR 2/3/4, LPDDR, ONFI, SATA
- HDMI
- CAN, CANFD, FLexRay
- SPI, I2C, SMBus

Military/Aerospace

- 1553

Multimedia

- H.264
- JPEG

Automotive:

- CAN/CAN-FD
- FlexCAN
- FlexRay
- Lin

AHB/APB Peripherals:

- AHB/AXI/APB bridges
- Inter Connect complete IP

Memory Controllers:

- SD/SDIO Controller
- SDRAM Controller
- DDR / SDRAM Controller
- NAND Flash Controller
- Flash/EEPROM/SRAM Controller
- PCMCIA/CompactFlash Host Adapter
- PCMCIA/CompactFlash Slave Controller

CPU Cores:

VarMan – 30% Faster High Sigma Monte Carlo Analysis and Yield Analysis with Silvaco!

Up to 30%+: The innovative approach of VarMan to Monte-Carlo analysis has been designed to provide equivalent results as a classical Monte Carlo Analysis, but with up to 30x+ time speed-up. Save simulation time, save money!

VarMan is a new generation tool that provides efficient and reliable solutions for analog, RF, standard cells, IO and memory designers.

It comprises a comprehensive suite of analysis tools that allow the designer to accurately address statistical design variations and to make the right design decision upfront. Its user-friendly framework for data loading and viewing.

Breakthrough analysis techniques: Impressive Simulation time speed-up and unique engine for local variability analysis

- **Reliable and mature technology:** Tested and validated by major players on most advanced technology nodes (FinFET, FDSOI). Complete control of results accuracy.

- **Smart simulation manager:** Simulation results management and high simulation throughput through LSF/SGE cluster.

- 32 bit – ColdFire V4
- 8 bit - 8051
- 8 bit- HC68HC11
- 8 bit - PIC Processor
- 8 bit – Z80
- 16 bit – D6800

Clock Synchronization:

- IEEE 1588 Slave
- IEEE 1588 Master
- IEEE 1588Master/Salve
- IEEE 1588 PTP Stack
- IEEE 1588 L2/L3 Solution

Peripherals:

- HDLC/SDLC
- Smart Card Reader Unit
- EEPROM SPI Ctrl
- LCD Ctrl
- Floating Point Unit
- I2C/I3C Master/Slave
- SPI Master/Slave
- CAN/CAN FD bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller 10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Salve
- On Chip Bus Analyzer

PCI Bus Controllers and Peripherals:

- PCI Express
- PCI-X Host Bridge Master/Target
- PCI Host Bridge Master/Target
- PCI-PCI Bridge

• **User friendly GUI:** Fast and easy to use 'load and analyze' use model. Clear presentation of results with access to data and waveforms into golden viewers

• **Simulator and environment independent:** Supports all golden spice simulators and design environments. Supports most advanced market design kits. Batch mode support

High-Sigma Yield Estimation

Robust & fast high-sigma yield estimator: given design performance limit, this analysis will quickly verify and estimate the yield to 4-6+ sigma with a very limited number of simulations. Capturing the process configurations that trigger parametric failures, to accurately estimate the yield and to predict extremely rare events.

Application:

HS Yield Analysis:
 • RAM bitcell, Global F. 28nm
 • 5.4 sigma – eq. of 930M MC runs

KAL is representing Silvaco in Israel.

www.KALtech.co.il

We are looking forward to hear from you.
 Contact us for more information.

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Until the next eNews,

Thanks you for your attention.

- PCI-ISA Bridge
- PCI Bus Arbiter

KAL

Modulation:

- ADPSM

Analog IP Cores:

- Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.
- We are expert in custom analog IP

[Contact us for data sheet](#)