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SD/SDIO/MMC Slave Controller IP Core Enables High Performance SD and MMC Card Design

Eureka Technology Inc., a leading intellectual property (IP) core provider, today announces the immediate availability of SD/SDIO/MMC slave controller core that supports Secure Digital (SD™) and Multi-Media Card (MMC™). Eureka has provided SD and MMC IP cores to many licensees since 2004. This latest addition of the slave controller completes the product line. A hardware development board for SD/SDIO/MMC development will also be available soon.

The slave controller facilitates the design of SD memory, SDIO and Multi-Media (MMC) cards and reduces the development time of any SD and MMC device projects. By using this IP core, customers no longer have to spend time on handling the SD or MMC bus protocol since such function is provided by the core. This allows our customers to focus primary on the added value of their products.

Ready to be integrated into an FPGA or ASIC, this controller handles the SD and MMC bus protocol on the card interface side and forward the request from the host to the user interface as simple memory read and write accesses. The core contains the standard SD and MMC register set and the core process register accesses locally, freeing the user logic from such house keeping tasks.

This IP core supports the latest SD and SDIO specification (version 2.0) and the latest MMC specification (version 4.2) for up to 8-bit data transfer. Many advanced features such as high capacity (SDHC), high speed mode, suspend and resume, SDIO and MMC Interrupt, SD Combo card, and multi-function card are supported. This slave controller core contains internal data buffer to improve data bandwidth and simplifies user logic design. The buffer size can vary from 1Kbytes to 32K bytes according to customer's need.

For more information about the SD/SDIO/MMC slave controller core, please visit <http://www.eurekatech.com/products/peripheral/default.htm>

About Eureka Technology

Eureka Technology Inc. is a leading intellectual property (IP) provider for ASIC, FPGA

and system designers. The company specializes in the integration and customization of standard IP core to meet customer requirements. Eureka offers a wide range of silicon proven system core logic and peripheral function cores for different CPU and bus standards including PowerPC™, AHB™, AXI™, PCI™, PCI-X™, PCI Express™, Cardbus™, SDR/DDR SDRAM, NAND Flash, Secure Digital (SD™), MMC, CompactFlash™ and PCMCIA™. These IP cores are designed to improve the design time-to-market, eliminate design risks, and reduce development costs for System-on-chip (SoC) designs. Located in Silicon Valley, California, Eureka Technology has pioneered the use of IP cores as a standard methodology in IC design and has licensed hundreds of IP cores to many leading companies in the semiconductor and electronic industries. With customer base in the US, Europe, Japan and other parts of Asia, the company has built many long term business relationships with its customers after their initial successes.